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Page: 38

Year of Publication: 1997

ISBN:0-8186-7852-6

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We present a unified model to analyze three different buffered allocation strategies, first-in-first-out (FIFO), statically allocated fully connected (SAFC), and statically allocated multi-queue (SAMQ), and two flow control schemes, local flow control and global flow control, under bursty traffic patterns. We also conduct comparisons between the local and global flow control schemes and among these three buffer allocation strategies. It has shown that the network can improve its performance by applying the local flow control and using the multi-queue buffered allocation strategy.

### ↑ INDEX TERMS

#### Keywords:

[ATM networks](#), [FIFO](#), [asynchronous transfer mode](#), [bursty traffic patterns](#), [first-in-first-out](#), [global flow control](#), [local flow control](#), [modeling](#), [multi-queue buffered allocation](#), [network performance](#), [statically allocated fully connected](#), [statically allocated multi-queue](#)

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## Modeling the buffer allocation strategies and flow control schemes in ATM networks

W. Amos Tiao, Dept. of Comput. Sci., Texas A&M Univ., College Station, TX, USA

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### Abstract

We present a unified model to analyze three different buffered allocation strategies, first-in-first-out (FIFO), statically allocated fully connected (SAFC), and statically allocated multi-queue (SAMQ), and two flow control schemes, local flow control and global flow control, under bursty traffic patterns. We also conduct comparisons between the local and global flow control schemes and among these three buffer allocation strategies. It has shown that the network can improve its performance by applying the local flow control and using the multi-queue buffered allocation strategy.

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**Index Terms-** asynchronous transfer mode; network performance; ATM networks; modeling; first-in-first-out; FIFO; statically allocated fully connected; statically

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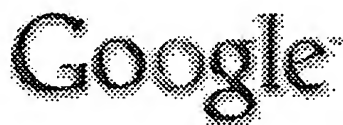
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<u>10604557</u>	Not Issued	30	07/30/2003	METHOD AND SYSTEM OF EFFICIENT PACKET REORDERING	GEORGIU, CHRISTOS J
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<u>10604491</u>	Not Issued	30	07/25/2003	SELF-CONTAINED PROCESSOR SUBSYSTEM AS COMPONENT FOR SYSTEM-ON-CHIP DESIGN	GEORGIU, CHRISTOS J.
<u>10674962</u>	Not Issued	20	09/30/2003	Method and system for on-demand allocation of a dynamic network of services	GEORGIU, CHRISTOS J.
<u>10768828</u>	Not Issued	20	01/30/2004	Single chip protocol converter	GEORGIU, CHRISTOS J.
<u>06321109</u>	Not Issued	161	11/13/1981	SWITCHING ARCHITECTURE FOR MULTIPROCESSOR COMPUTER SYSTEMS	GEORGIU, CHRISTOS J.
<u>06544652</u>	4605928	150	10/24/1983	FAULT-TOLERANT ARRAY OF CROSS-POINT SWITCHING MATRICES	GEORGIU, CHRISTOS





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<u>09682688</u>	Not Issued	83	10/05/2001	Programmable storage network protocol handler architecture	SALAPURA, VALENTINA
<u>09682689</u>	6904040	150	10/05/2001	PACKET PREPROCESSING INTERFACE FOR MULTIPROCESSOR NETWORK HANDLER	SALAPURA, VALENTINA
<u>09683863</u>	6836808	150	02/25/2002	PIPELINED PACKET PROCESSING	SALAPURA, VALENTINA
<u>09940709</u>	Not Issued	71	08/28/2001	Configurable memory array	SALAPURA, VALENTINA
<u>10085606</u>	Not Issued	71	02/26/2002	Method and apparatus for prioritized instruction issue queue in a processor	SALAPURA, VALENTINA
<u>10150270</u>	6948082	150	05/17/2002	METHOD AND APPARATUS FOR SOFTWARE-ASSISTED THERMAL MANAGEMENT FOR ELECTRONIC SYSTEMS	SALAPURA, VALENTINA
<u>10280187</u>	Not Issued	80	10/24/2002	Method and apparatus for enabling access to global data by a plurality of codes in an integrated executable for a heterogeneous architecture	SALAPURA, VALENTINA
<u>10280242</u>	Not Issued	30	10/24/2002	Method and apparatus for overlay management within an integrated executable for a heterogeneous architecture	SALAPURA, VALENTINA
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